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**LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT**

APPLICANT:

Mikko Waltari

FILING DATE:

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GROUP:

To be assigned

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U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date/ Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code			
JLW	AA	5,212,486	05-18-1993	Nagaraj 341/172	
	AB	5,510,789	04-23-1996	Lee 341/120	
	AC	5,644,308	07-01-1997	Kerth et al. 341/120	
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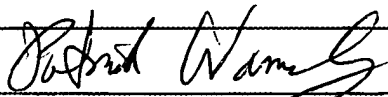
FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document	Publication Date/Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ¹

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
JLW	AF	KRISHNASWAMI NAGARAJ, Senior Member; Efficient Circuit Configurations for Algorithmic Analog to Digital Converters; IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing; December 1993; pp. 1-9; Vol. 40, No. 12	

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